



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Mark S. Moir et al.

Title:

SOFTWARE TRANSACTIONAL MEMORY FOR DYNAMICALLY

SIZABLE SHARED DATA STRUCTURES

Application No.: 10/621,072

Filed:

July 16, 2003

Examiner:

Horace L. Flournoy

Group Art Unit:

2189

Atty. Docket No.: 004-8428

Confirmation No.:

1240

January 27, 2006

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PTO/SB/08B ((Substitut	te for form 1449/PTO)	Attorney Docket No.:	004-8428		
			Application No.:	10/621,072		
INI	FORMA	TION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor	r: Mark S. Moir		
PE 4		·	Filing Date:	July 16, 2003		
	8/		Group Art Unit:	2189		
JAN 3 1 2006) []		Examiner Name:	Horace L. Flourn	oy	
heet 1 of 1	3 /		Date Submitted:	January 27, 2006		
		NON PATENT LITERATURE DO	CUMENTS			
Examiner Initials*	Cite No. ¹	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
	1	AFEK, Yehuda, "Atomic Snapshots of Shared Memory," Journa	of the ACM, vol. 40, no. 4, pp.	873-890, 1993.	. •	
	2	AGESEN, Ole at el., "An Efficient Meta-Lock for Implementing Ubiquitous Synchronization," ACM SIGPLAN Notices, vol. 34, no. 10, pp. 207-222, October 1999.				
	3	ANDERSON, James H. et al., "Universal Constructions for Large Objects," <i>IEEE Transactions on Parallel and Distributed Systems</i> , vol. 10, no. 12, pp. 1317-1332, 1999.				
	4	MELLOR-CRUMMEY, J., et al., "Algorithms for Scalable Synchronization on Shared-Memory Multiprocessors, ACM Transactions on Computer Systems, vol. 9, no. 1, pp. 21-65, February 1991.				
	PRAKASH, Sundeep et al., "Non-Blocking Algorithms for Concurrent Data Structures," Technical Report University of Florida, July 1, 1991 [URL: http://citeseer.ist.psu.edu/prakash91nonblocking.html]. PRAKASH, Sundeep et al., "A Nonblocking Algorithm for Shared Queues Using Compare-and-Swap," IE Transactions on Computers, vol. 43, no. 5, pp. 548-559, May 1994. SHANN, Chien-Hua et al., "A Practical Nonblocking Queue Algorithm Using Compare-and Swap," Proce of the Seventh International Conference on Parallel and Distributed Systems p. 470, IEEE Computer Soc Washington, D.C., 2000.					
	8	SHAVIT, N., et al., "Elimination Trees and the Construction of Pools and Stacks," <i>Theory of Computing Systems</i> , vol. 30, pp. 645-670, 1997.				
	9	STONE, Janice M., "A simple and correct shared-queue algorith the 1990 ACM/IEEE Conference on Supercomputing, pp. 495-5 1990.				
	10	VALOIS, John D., "Lock-Free Linked Lists Using Compare-and- Symposium on Principles of Distributed Computing, pp. 214-222				
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